

SYSTEM AND METHOD FOR TESTING A MEMORY USING DMA

Abstract

5 A computer system that includes a processor, a first bus coupled to the
processor, a memory controller coupled to the first bus, a memory coupled to the
memory controller, a first input/output (I/O) controller coupled to the first bus,
and a test module coupled to the first I/O controller is provided. The test module
is configured to cause tests to be performed on the memory using the first bus.

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